

S/N Unknown

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Kishore K. Chakravorty

Examiner: Unknown

Serial No.: Unknown

Group Art Unit: Unknown

Filed: Herewith

Docket: 884.267US1

Title: ELECTRONIC ASSEMBLY COMPRISING INTERPOSER WITH
EMBEDDED CAPACITORS AND METHODS OF MANUFACTURE

JC862 U.S. PRO
09/628705



INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
Washington, D.C. 20231

In compliance with the duty imposed by 37 C.F.R. § 1.56, and in accordance with 37 C.F.R. §§ 1.97 *et. seq.*, the enclosed materials are brought to the attention of the Examiner for consideration in connection with the above-identified patent application. Applicant respectfully requests that this Information Disclosure Statement be entered and the documents listed on the attached Form 1449 be considered by the Examiner and made of record. Pursuant to the provisions of MPEP 609, Applicant further request that a copy of the 1449 form, initialed by the Examiner to indicate that all listed citations have been considered, be returned with the next official communication.

Please charge any additional fees or credit any overpayment to Account No. 19-0743.

The Examiner is invited to contact the Applicant's Representative at the below-listed telephone number if there are any questions regarding this communication.

Respectfully submitted,

KISHORE K. CHAKRAVORTY

By his Representatives,

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Date

July 31, 2000

By

Ann M. McCrackin

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Reg. No. 42,858

CERTIFICATE UNDER 37 CFR 1.10:

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Date of Deposit: July 31, 2000

I hereby certify that this paper or fee is being deposited with the United States Postal Service "Express Mail Post Office to Addressee" service under 37 CFR 1.10 on the date indicated above and is addressed to the Assistant Commissioner for Patents, Washington, D.C. 20231.

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Form 1449*	Atty. Docket No.: 884.267US1	Serial No. Unknown
INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use several sheets if necessary)	Applicant: Kishore K. Chakravorty	
	Filing Date: Herewith	Group: Unknown

U.S. PATENT DOCUMENTS

**Examiner Initial	Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate
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FOREIGN PATENT DOCUMENTS

**Examiner Initial	Document Number	Date	Country	Class	Subclass	Translation Yes	No
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OTHER DOCUMENTS

(Including Author, Title, Date, Pertinent Pages, Etc.)

**Examiner Initial	
	Amey, D., et al., "Advances in MCM Ceramics", <u>Solid State Technology</u> , 143-146, (1997)
	Baniecki, J., et al., "Dielectric Relaxation of Ba _{0.7} Sr _{0.3} TiO ₃ Thin Films from 1 MHz to 20 GHz", <u>Appl. Phys. Letter</u> 72(4), 1998 American Institute of Physics, 198-500, (Jan. 1998)
	Chan, Y., et al., "Fabrication and Characterization of Multilayer Capacitors Buried in a Low Temperature Co-Fired Ceramic Substrate", <u>Active and Passive Elec. Comp. Vol. 20</u> , 215-224, (1998)
	Choi, K.L., et al., "Characterization of Embedded Passives Using Macromodels in LTCC Technology", <u>IEEE Transactions on Components, Packaging, and Manufacturing Technology, Vol. 21</u> , 258-268, (Aug. 1998)
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	Koschmieder, T., et al., "Ceramic Substrate Thickness, Test Board Thickness, and Part Spacing: A Screening Doe", <u>Proceedings of SMTA International Conference</u> , 6 pgs., (Sept. 1999)
	Mistler, R.E., "Tape Casting: Past, Present, Potential", <u>The American Ceramic Society Bulletin</u> , 82-86, (Oct. 1998)
	Nishimura, T., et al., "3.5 V Operation Driver-Amplifier MMIC Utilizing SrTiO ₃ Capacitors for 1.95 GHz Wide-Band CDMA Cellular Phones", <u>1998 IEEE MTT-S Digest</u> , 447-450, (1998)
	Rector, Jr., J., et al., "Integrated and Integral Passive Components: A Technology Roadmap", <u>1997 Electronic Components and Technology Conference</u> , 713-723, (1997)

Examiner	Date Considered
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*Substitute Disclosure Statement Form (PTO-1449)

**EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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	Scrantom, S., et al., "Manufacture of Embedded Integrated Passive Components into Low Temperature Co-Fired Ceramic Systems", <u>1998 International Symposium on Microelectronics</u> , 459-466, (1998)
	Sugai, K., et al., "Multilayer Alumina Substrates for ECU", <u>1998 IEEE/CPMT Berlin Int'l Electronics Manufacturing Technology Symposium</u> , 109-112, (1998)
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	Ueda, T., et al., "GaAs MMIC Chip-sets for Mobile Communication Systems with On-chip Ferroelectric Capacitors", <u>Integrated Ferroelectrics, Vol. 7</u> , 45-60, (1995)
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